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| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
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| <u>10272029</u> | Not Issued | 020 | 10/15/2002 | SIMULTANEOUS REAL-TIME TRACE AND DEBUG FOR MULTIPLE PROCESSING CORE SYSTEMS ON A CHIP | NEWLIN, JOHN |
| <u>09680126</u> | Not Issued | 030 | 10/03/2000 | DEBUGGING APPARATUS AND METHOD FOR SYSTEMS OF CONFIGURABLE PROCESSORS | NEWLIN, JOHN |
| <u>07255472</u> | <u>4982343</u> | 150 | 10/11/1988 | METHOD AND APPARATUS FOR DISPLAYING A PLURALITY OF GRAPHIC IMAGES | NEWLIN, JOHN K. |

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